



CXL2.0 Memory Expansion Modules

Empowering cloud AI to achieve better performance

Introduction

The FORESEE CXL 2.0 memory expansion module is developed based on DDR5 DRAM and supports a PCIe 5.0 x8 Interface with a theoretical bandwidth of up to 32GB/s. It can seamlessly connect with backplanes and server motherboards that support the CXL specification and E3.S interface, thereby reducing overall system TCO and minimizing idle memory resources. This improves memory utilization and expands server memory capacity and bandwidth performance, unlocking the potential of applications such as HPC, cloud computing, and AI.

Features

- Supports memory pooling sharing
- Based on SDP DRAM
- In-house architecture design
- Supports CXL specifications and E3.S interface

Highlights

- High-speed: theoretical bandwidth of 32GT/s for Gen 5.0
- Innovative stacking technology doubles the industry-standard capacity, supporting up to 512GB.
- 192GB high capacity based on SDP DRAM
- Open source API, compatible with CXL 1.1/2.0
- Low cost and high expansion to improve storage ROI
- Low latency, 250ns~270ns(performance varies according to different capacity)

Application



AI Terminals



Cloud Computing



Server

Product Parameter

Product Series	CXL2.0 Memory Expansion Module		
Capacity	64GB	128GB	192GB
Interface	PCIe 5.0×8 lanes		
Type	EDSFF(E3.S 2T)		
DDR	DDR5		
Bit width	32GB/s		
Latency	250ns	270ns	
CL Value	16ns		
Timing	46-45-45		
DDR Operating Voltage	1.1V		
Gold Fingers (Edge Connectors)	30μ"		
Operating Temperature	Tc 0°C~85°C		
Size	76×112.75×16.5mm		

*The data comes from internal test. The actual performance may vary due to equipment differences.



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